

FORM HDP-1449 (Based on Form PTO-1449)

PATENT AND TRADEMARK OFFICE
INFORMATION DISCLOSURE CITATION
 Use several sheets if necessary)

OCT 03 2003

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ATTORNEY DOCKET NO.

SERIAL NO.

MP0074.C1

10/601,286

APPLICANT

Farbod ARAM et al.

FILING DATE

GROUP

June 20, 2003

UNKNOWN

U.S. PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date (mm/dd/yyyy)	Name	Class/ Subclass	(If appropriate) Filing Date
	NH	6,597,562	07/22/2003	Hu et al.		

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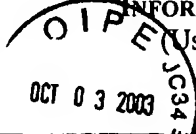
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	NH	JP 2-178963	07/11/1990	JAPAN		X (Abstract)	
		JP 64-84616	03/29/1989	JAPAN			X
		JP 59-105341	06/18/1984	JAPAN		X (Abstract)	
		JP 01096943	04/14/1989	JAPAN		X (Abstract)	
		JP 02086160	03/27/1990	JAPAN		X (Abstract)	
		JP 02090561	03/30/1990	JAPAN		X (Abstract)	
		JP 02178963	07/11/1990	JAPAN		X (Abstract)	
		JP 02191369	07/27/1990	JAPAN		X (Abstract)	
		JP 03096267	04/22/1991	JAPAN		X (Abstract)	
		JP 03133169	06/06/1991	JAPAN		X (Abstract)	
		JP 03181165	08/07/1991	JAPAN		X (Abstract)	
		JP 61055936	03/20/1986	JAPAN		X (Abstract)	
		JP 61263251	11/21/1986	JAPAN		X (Abstract)	
		JP 54-101153	08/09/1979	JAPAN			X
		JP 59-197122	11/08/1984	JAPAN			X
		JP 60-1825	01/08/1985	JAPAN			X
	NH	JP 60-4207	01/10/1985	JAPAN			X

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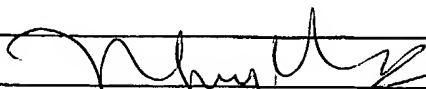
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	June 20, 2003	UNKNOWN

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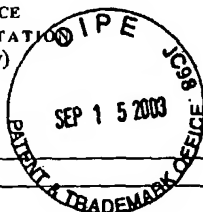
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						Yes	No
	NH	JP 60-92604	05/24/1985	JAPAN	—		X
	NH	JP 46-43172	12/21/1971	JAPAN	—		X
	NH	JP 59-91718	06/21/1984	JAPAN	—		X

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Ref. Desig.	Examiner's Initials	
		Shiba, Takashi et al. "Study on Transformer Ratio and Capacitance in Equivalent Circuit Model for SAW Transducer." 10 th Symposium on Ultrasonic Electronics (Tokyo 1989) <u>Japanese Journal of Applied Physics</u> , Vol. 29 (1990) Supplement 29-1, pp. 129-132.
		N. MONTH / DATE

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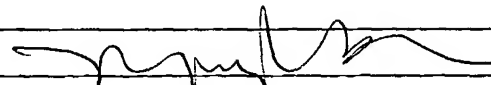
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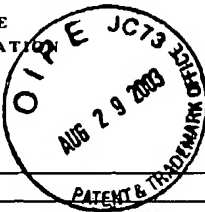
FOREIGN PATENT DOCUMENTS							
Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes	No
	12/18	JP 1-120858	05/12/1989	JAPAN	—	X (Abstract)	
	12/18	JP 3-257855	11/18/1991	JAPAN	—	X (Abstract)	

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	10A	5,208,725	05/ 04/ 1993	Akcasu		

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Sheet 1 of 2

ATTORNEY DOCKET No.

MP0074.C1

SERIAL No.

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Aram, Farbod et al.

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Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.	NH	5,583,359	12-1996	Ng et al.	—	
2.		5,903,493	05-1999	Lee	—	
3.		6,001,702	12-1999	Cook et al.		
4.		6,150,707	11-2000	Cook et al.		
5.		6,266,226	07-2001	Hayashi		
6.		6,266,227	07-2001	Konushi et al.		
7.		6,312,963	11-2001	Chou et al.		
8.		6,414,806	07-2002	Gowda et al.		
9.		6,445,056	09-2002	Nakashima		
10.		6,459,561	10-2002	Galvagni et al.	—	
11.	NH	6,501,610	12-2002	Sugawara et al.	—	

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Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes	No
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1.	NH	Bhupendra K. Ahuja, "An Improved Frequency Compensation Technique for CMOS Operational Amplifiers", IEEE J. Solid-State Circuits, vol. SC 18, no. 6, pp. 629-633, Dec. 1983.

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Ref. Desig.	Examiner's Initials	
2.	UH	Runsheng He, Nersi Nazari and Sehat Sutardja, "A DSP Receiver for 1000BASE-T PHY," Submitted to ISSCC 2001. <i>Date / Month / year missing</i>
3.	↓	S. Sutarja and P.R. Gray, "A pipelined 13-bit, 250-ks/s, 5-V, analog-to-digital converter," IEEE J. Solid-State Circuits, vol. 23, pp. 1316-1323, Dec. 1988.
4.	↓	S.H. Lewis and P.R. Gray, "A pipelined 5-Msample/s 9-bit analog to digital converter," IEEE J. Solid-State Circuits, vol. SC-22, pp. 954-961, Dec. 1987.
5.	UH	S.H. Lewis, R. Ramachandran "A 10-b 20-Msample/s Analog-to-Digital Converter," IEEE J. Solid-State Circuits, vol. 27, pp. 351-358, March 1992.

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